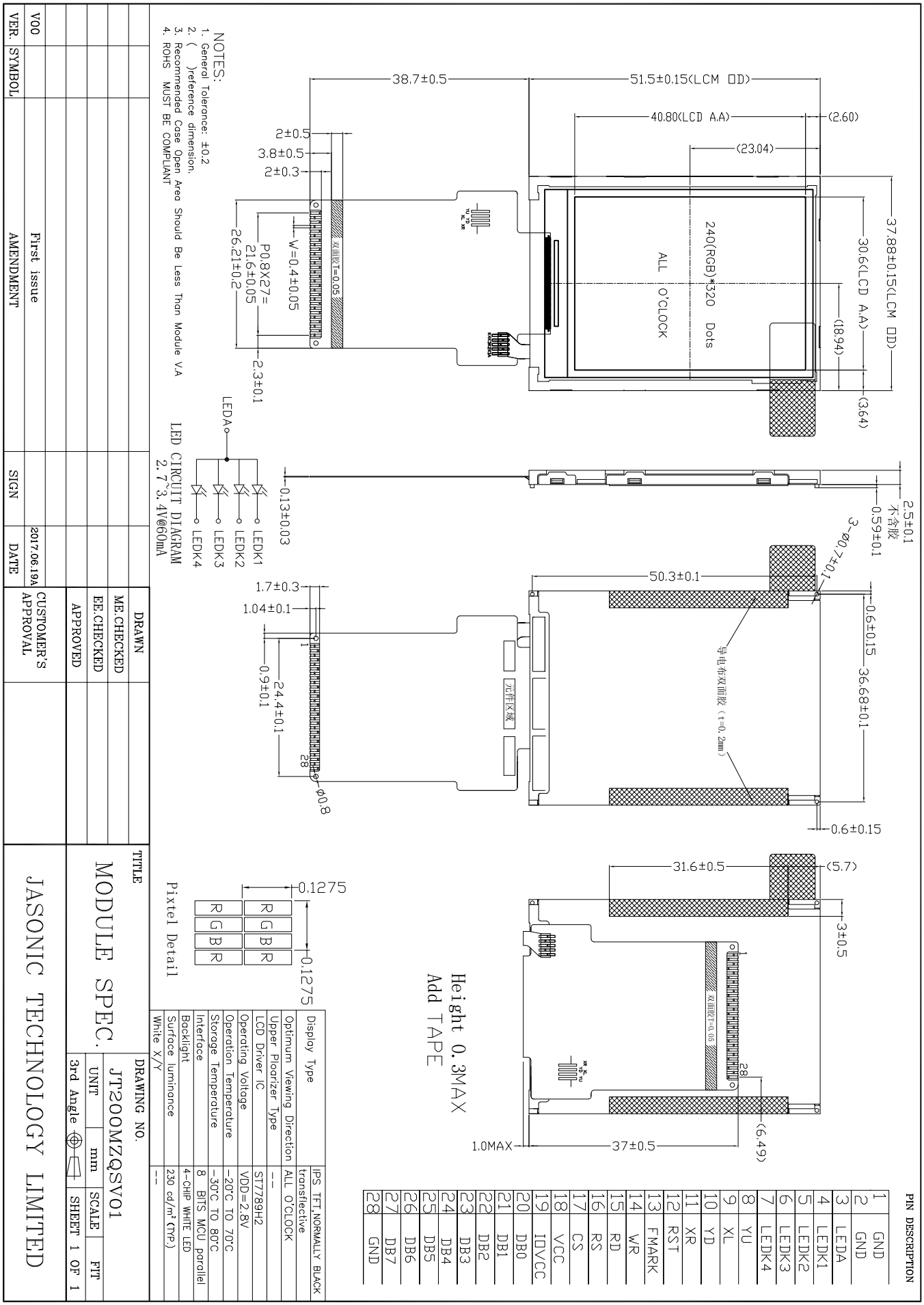
NOTES:
1. General Tolerance: ±0.2
2. ( )reference dimension.
3. Recommended Case Open Area Should Be Less Than Module V.A
4. ROHS MUST BE COMPLIANT
38.7±0.5
51.5±0.15(LCM □D)
40.80(LCD A.A)
(2.60)
(23.04)
2±0.5
3.8±0.5
2±0.3
37.88±0.15(LCM □D)
30.6(LCD A.A)
(18.94)
(3.64)
240(RGB)*320 Dots
ALL O'CLOCK
双面胶T=0.05
W=0.4±0.05
P0.8X27=
21.6±0.05
26.21±0.2
2.3±0.1
LEDA
LED CIRCUIT DIAGRAM
2.7~3.4V@60mA
LEDK1
LEDK2
LEDK3
LEDK4
0.13±0.03
2.5±0.1
不含胶
0.59±0.1
3-φ0.7±0.1
50.3±0.1
0.6±0.15
36.68±0.1
导电布双面胶（t=0.2mm）
元件区域
1.7±0.3
1.04±0.1
0.9±0.1
24.4±0.1
28
φ0.8
31.6±0.5
(5.7)
3±0.5
(6.49)
1.0MAX
37±0.5
Height 0.3MAX
Add TAPE
Pixtel Detail
0.1275
R G B R
PIN DESCRIPTION
1 GND
2 GND
3 LEDA
4 LEDK1
5 LEDK2
6 LEDK3
7 LEDK4
8 YU
9 XL
10 YD
11 XR
12 RST
13 FMARK
14 WR
15 RD
16 RS
17 CS
18 VCC
19 IOVCC
20 DB0
21 DB1
22 DB2
23 DB3
24 DB4
25 DB5
26 DB6
27 DB7
28 GND
Display Type IPS TFT,NORMALLY BLACK transflective
Optimum Viewing Direction ALL O'CLOCK
Upper Ploarizer Type --
LCD Driver IC ST7789H2
Operating Voltage VDD=2.8V
Operation Temperature -20°C TO 70°C
Storage Temperature -30°C TO 80°C
Interface 8 BITS MCU parallel
Backlight 4-CHIP WHITE LED
Surface luminance 230 cd/m² (TYP.)
White X/Y --
DRAWN
ME.CHECKED
EE.CHECKED
APPROVED
CUSTOMER'S APPROVAL
TITLE
MODULE SPEC.
DRAWING NO.
JT200MZQSV01
UNIT mm
SCALE FIT
3rd Angle
SHEET 1 OF 1
JASONIC TECHNOLOGY LIMITED
V00 First issue 2017.06.19A
VER. SYMBOL AMENDMENT SIGN DATE