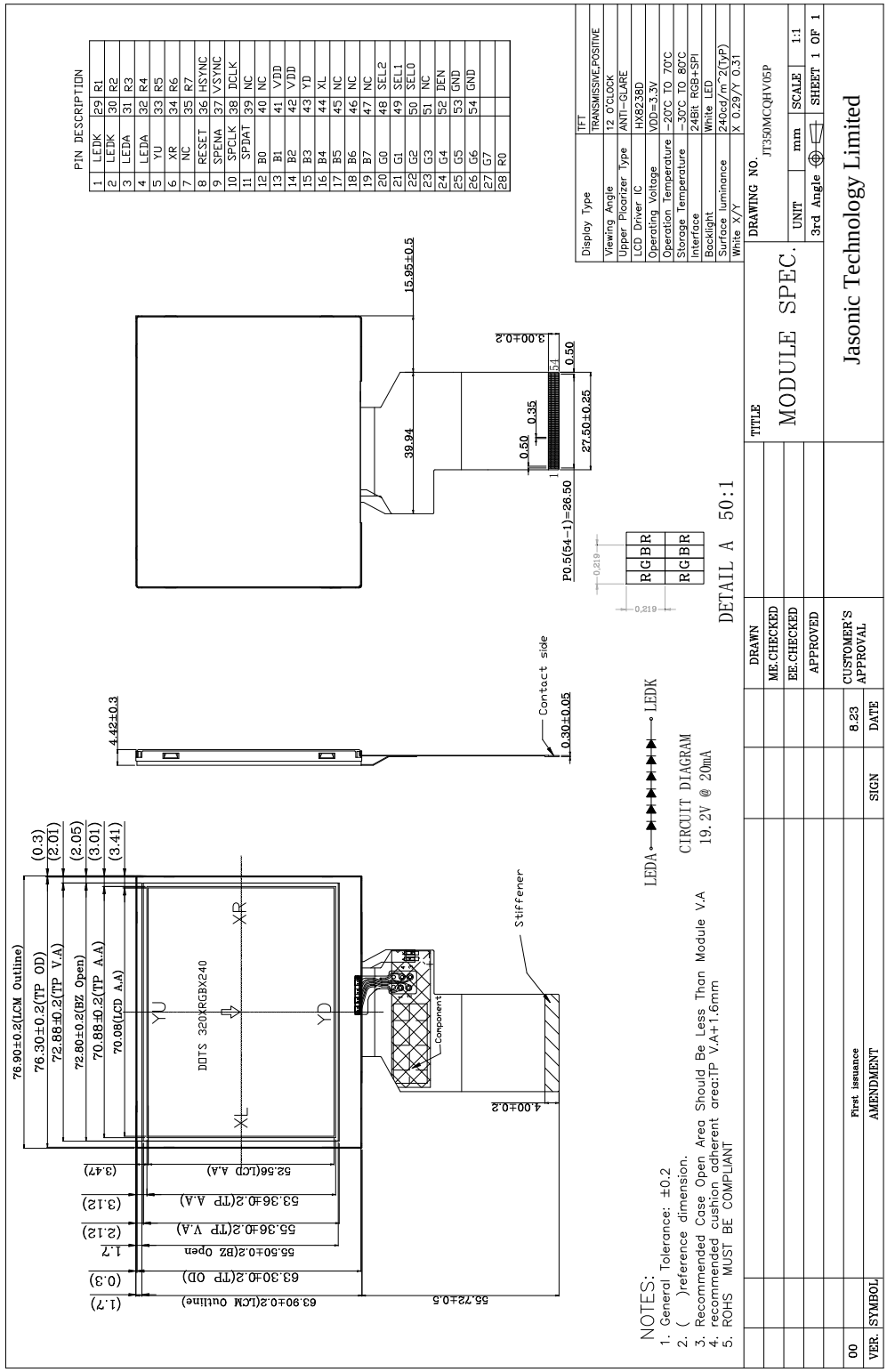
PIN DESCRIPTION
1 LEDK 29 R1
2 LEDK 30 R2
3 LEDA 31 R3
4 LEDA 32 R4
5 YU 33 R5
6 XR 34 R6
7 NC 35 R7
8 RESET 36 HSYNC
9 SPENA 37 VSYNC
10 SPCLK 38 DCLK
11 SPDAT 39 NC
12 B0 40 NC
13 B1 41 VDD
14 B2 42 VDD
15 B3 43 YD
16 B4 44 XL
17 B5 45 NC
18 B6 46 NC
19 B7 47 NC
20 G0 48 SEL2
21 G1 49 SEL1
22 G2 50 SEL0
23 G3 51 NC
24 G4 52 DEN
25 G5 53 GND
26 G6 54 GND
27 G7
28 R0
Display Type TFT
Viewing Angle TRANSMISSIVE,POSITIVE
Upper Ploarizer Type 12 O'CLOCK
LCD Driver IC ANTI-GLARE
Operating Voltage HX8238D
Operation Temperature VDD=3.3V
Storage Temperature -20°C TO 70°C
Interface -30°C TO 80°C
Backlight 24Bit RGB+SPI
Surface luminance White LED
White X/Y 240cd/m^2(TyP)
X 0.29/Y 0.31
15.95±0.5
39.94
3.00±0.2
0.50
0.35
27.50±0.25
P0.5(54-1)=26.50
DETAIL A 50:1
4.42±0.3
Contact side
0.30±0.05
76.90±0.2(LCM Outline)
76.30±0.2(TP OD)
72.88±0.2(TP V.A)
72.80±0.2(BZ Open)
70.88±0.2(TP A.A)
70.08(LCD A.A)
DOTS 320XRGBX240
YU
YD
XR
XL
Component
Stiffener
4.00±0.2
52.56(LCD A.A)
53.36±0.2(TP A.A)
55.36±0.2(TP V.A)
55.50±0.2(BZ Open
63.30±0.2(TP OD)
63.90±0.2(LCM Outline)
55.72±0.5
LEDA
LEDK
CIRCUIT DIAGRAM
19.2V @ 20mA
NOTES:
1. General Tolerance: ±0.2
2. ( )reference dimension.
3. Recommended Case Open Area Should Be Less Than Module V.A
4. recommended cushion adherent area:TP V.A+1.6mm
5. ROHS MUST BE COMPLIANT
TITLE
MODULE SPEC.
DRAWING NO. JT350MCQHV05P
UNIT mm
SCALE 1:1
3rd Angle
SHEET 1 OF 1
Jasonic Technology Limited
DRAWN
ME.CHECKED
EE.CHECKED
APPROVED
CUSTOMER'S APPROVAL
DATE 8.23
SIGN
00 First issuance
VER. SYMBOL AMENDMENT